AMENDMENTS TO THE CLAIMS

This listing of claims will replace all prior versions and listings of claims in the application:

LISTING OF CLAIMS:

1. (canceled).

2. (previously presented): A pulse width modulating device comprising:

a clock generating device which generates a first clock signal;

an operation device which operates the first clock signal and generates at least one processing clock signal whose phase is different than a phase of the first clock signal;

a pulse width modulating signal output device which makes a pulse of a pulse width modulating signal rise synchronously with one of the first clock signal and the processing clock signal generated by said operation device, and makes the pulse of the pulse width modulating signal fall synchronously with a remaining one of the first clock signal and the processing clock signal generated by said operation device; and

a counter which, after the pulse of the pulse width modulating signal is made to rise, counts a number of pulses of the remaining one of the first clock signal and the processing clock signal generated by said operation device,

wherein after said counter has counted a predetermined number of pulses, said pulse width modulating signal output device makes the pulse fall synchronously with the remaining one of the first clock signal and the processing clock signal generated by said operation device.

3-11 (canceled).

12 (previously presented): A pulse width modulating method comprising

(a) generating a first clock signal;

(b) operating the first clock signal and generating at least one clock signal whose phase is different than a phase of the first clock signal;

(c) making a pulse of a pulse width modulating signal rise synchronously with one of the first clock signal and the clock signal generated in step (b), and making the pulse of the pulse width modulating signal fall synchronously with a remaining one of the first clock signal and the clock signal generated in step (b); and

(d) in step (c), after the pulse is made to rise, counting a number of pulses of the remaining one of the first clock signal and the clock signal generated in step (b),

wherein in step (c), after a predetermined number of pulses have been counted by step (d), the pulse is made to fall synchronously with the remaining one of the first clock signal and the clock signal generated in step (b).

13-20 (cancelled).

21. (previously presented): A pulse width modulating device comprising:

a clock generating device which generates a first clock signal;

an operation device which operates the first clock signal and generates at least one processing clock signal whose phase is different than a phase of the first clock signal;

a pulse width modulating signal output device which makes a pulse of a pulse width modulating signal rise synchronously with one of the first clock signal and the processing clock signal generated by said operation device, and makes the pulse of the pulse width modulating signal fall synchronously with a remaining one of the first clock signal and the processing clock signal generated by said operation device; and

a selector which selects and outputs to the pulse width modulating signal output device one of the first clock signal and the processing clock signal, based on a clock selection signal input to the selector.

22. (canceled).

23. (currently amended): An exposure device ~~according to claim 7,~~ comprising:

(a) a pulse width modulating device including:

(i) a clock generating device which generates a first clock signal;

(ii) an operation device which operates the first clock signal and generates at least one processing clock signal whose phase is different than a phase of the first clock signal; and

(iii) a pulse width modulating signal output device which makes a pulse of a pulse width modulating signal rise synchronously with one of the first clock signal and the processing clock signal generated by said operation device, and makes the pulse of the pulse width modulating signal fall synchronously with a remaining one of the first clock signal and the processing clock signal generated by said operation device; and

(b) a light source for exposure which emits light in accordance with a pulse width of respective pulses of the pulse width modulating signal outputted by said pulse width modulating signal outputting device provided at said pulse width modulating device; and

a selector which selects and outputs to the pulse width modulating signal output device one of the first clock signal and the processing clock signal, based on a clock selection signal input to the selector.

24. (canceled).